

**IN THE CLAIMS:**

Please amend the claims in accordance with the following claim listing. This listing of the claims replaces and supersedes all prior listings:

1. (Canceled).
2. (Currently Amended) The method of claim [[1]] 43, wherein at least two of said plurality of processing elements are updated at different speeds.
3. (Previously Presented) The method of claim 2, wherein a processing object controls the update of said at least two of said processing elements.
4. (Previously Presented) The method of claim 2, wherein one of said at least two of said plurality of processing elements operates at an acquisition speed and another of said at least two of said plurality of processing elements operates at a display speed, and wherein the acquisition speed is higher than the display speed.
5. (Original) The method of claim 2, wherein said at least two of said plurality of processing elements are idle when not updated.
6. (Previously Presented) The method of claim 2, wherein one of said at least two of said plurality of processing elements is of a cumulative type running at a first speed, and another of said at least two of said plurality of processing elements is of a non-cumulative type running at a second speed, and wherein the first speed is higher than the second speed.

7-10 (Canceled).

11. (Currently Amended) The method of claim [[1]] 43, wherein one of said plurality of processing elements requests data from an upstream source when data is requested from it by a downstream processing element.

12 (Canceled).

13. (Currently Amended) The method of claim [[7]] 43, wherein at least one of said plurality of processing elements receives M inputs on an input pin and produces N output results on an output pin, where M is an integer equal to or greater than 1 and where N is an integer equal to or greater than 0.

14-22 (Canceled).

23. (Currently Amended) The processing web of claim [[22]] 45, wherein at least two of said plurality of processing elements are updated at different speeds.

24. (Original) The processing web of claim 23, wherein a processing object controls the update of said at least two of said processing elements.

25. (Previously Presented) The processing web of claim 23, wherein one of said at least two of said plurality of processing elements operates at an acquisition speed and another of said at least two of said plurality of processing elements operates at a display speed, and wherein the acquisition speed is higher than the display speed.

26. (Original) The processing web of claim 23, wherein said at least two of said plurality of processing elements are idle when not updated.

27. (Previously Presented) The processing web of claim 23, wherein one of said at least two of said plurality of processing elements is of a cumulative type running at a first speed, and another of said at least two of said plurality of processing elements is of a non-cumulative type running at a second speed, and wherein the first speed is higher than the second speed.

28-31 (Canceled)

32. (Currently Amended) The processing web of claim [[28]] 45, wherein one of said plurality of processing elements requests data from an upstream source when data is requested from it by a downstream processing element.

33-42 (Canceled).

43. (New) A method for configuring and performing processing in a digital oscilloscope processing apparatus, comprising the steps of:

receiving one or more input parameters;

defining a plurality of processing elements based upon said received parameters; and

connecting said plurality processing elements to define a processing web;

wherein at least one of said plurality of processing elements requests required data from an upstream source.

44. (New) The method of claim 43, wherein the upstream source transmits requested data to the one of the plurality of processing elements requesting data therefrom without an intervening buffer.

45. (New) A processing web defining processing in a digital oscilloscope processing apparatus, comprising:

a plurality of processing elements that are defined based upon one or more received input parameters, each of said processing elements performing a discrete processing function; and

a plurality of connections between said plurality of processing elements to define a flow of information therebetween;

wherein at least one of said plurality of processing elements requests required data from an upstream source.

46. (New) The processing web of claim 45, wherein the upstream source transmits requested data to the one of the plurality of processing elements requesting data therefrom without an intervening buffer.

47. (New) A processing web defining processing in a digital processing apparatus, comprising:

a plurality of processing elements that are defined based upon one or more received input parameters, each of said processing elements performing a discrete processing function; and

a plurality of connections between said plurality of processing elements to define a flow of information therebetween;

wherein at least one of said plurality of processing elements requests required data from an upstream source.

48. (New) The processing web of claim 47, wherein one of said plurality of processing elements requests data from an upstream source when data is requested from it by a downstream processing element.

49. (New) The processing web of claim 47, wherein the upstream source transmits requested data to the one of the plurality of processing elements requesting data therefrom without an intervening buffer.